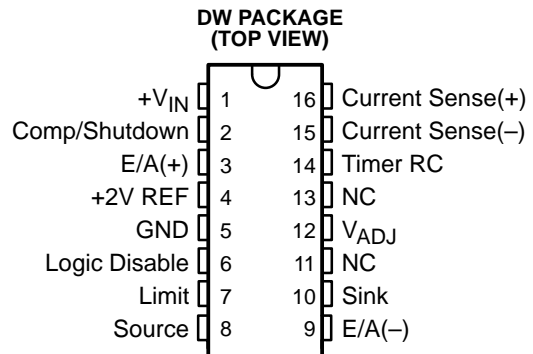


UC2832-EP PRECISION LOW DROPOUT LINEAR CONTROLLER

SGLS147A – JANUARY 2003 – REVISED MARCH 2003

- **Controlled Baseline**
 - One Assembly/Test Site, One Fabrication Site
- **Extended Temperature Performance of –40°C to 105°C**
- **Enhanced Diminishing Manufacturing Sources (DMS) Support**
- **Enhanced Product Change Notification**
- **Qualification Pedigree†**
- **Precision 1% Reference**
- **Over-Current Sense Threshold Accurate to 5%**
- **Programmable Duty-Ratio Over-Current Protection**
- **4.5 V to 36 V Operation**
- **100 mA Output Drive, Source or Sink**
- **Under-Voltage Lockout**
- **Adjustable Current Limit to Current Sense Ratio**
- **Separate +V_{IN} terminal**
- **Programmable Driver Current Limit**
- **Access to VREF and E/A(+)**
- **Logic-Level Disable Input**

† Component qualification in accordance with JEDEC and industry standards to ensure reliable operation over an extended temperature range. This includes, but is not limited to, Highly Accelerated Stress Test (HAST) or biased 85/85, temperature cycle, autoclave or unbiased HAST, electromigration, bond intermetallic life, and mold compound life. Such qualification testing should not be viewed as justifying use of this component beyond specified performance and environmental limits.



NC = No Connect

description

The UC2832 series of precision linear regulators include all the control functions required in the design of very low dropout linear regulators. Additionally, they feature an innovative duty-ratio current limiting technique which provides peak load capability while limiting the average power dissipation of the external pass transistor during fault conditions. When the load current reaches an accurately programmed threshold, a gated-astable timer is enabled, which switches the regulator's pass device off and on at an externally programmable duty-ratio. During the on-time of the pass element, the output current is limited to a value slightly higher than the trip threshold of the duty-ratio timer. The constant-current-limit is programmable on the UC2832 to allow higher peak current during the on-time of the pass device. With duty-ratio control, high initial load demands and short circuit protection may both be accommodated without extra heat sinking or foldback current limiting. Additionally, if the timer pin is grounded, the duty-ratio timer is disabled, and the IC operates in constant-voltage/constant-current regulating mode.

These IC's include a 2 Volt ($\pm 1\%$) reference, error amplifier, UVLO, and a high current driver that has both source and sink outputs, allowing the use of either NPN or PNP external pass transistors. Safe operation is assured by the inclusion of under-voltage lockout (UVLO) and thermal shutdown.

ORDERING INFORMATION‡

| T _A | PACKAGE§ | | ORDERABLE PART NUMBER | TOP-SIDE MARKING |
|----------------|----------|---------------|-----------------------|------------------|
| –40°C to 105°C | SOP – DW | Tape and reel | UC2832TDWREP | UC2832TEP |
| –40°C to 105°C | SOP – DW | Tube | UC2832TDWEP | UC2832TEP |

‡ Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**TEXAS
INSTRUMENTS**

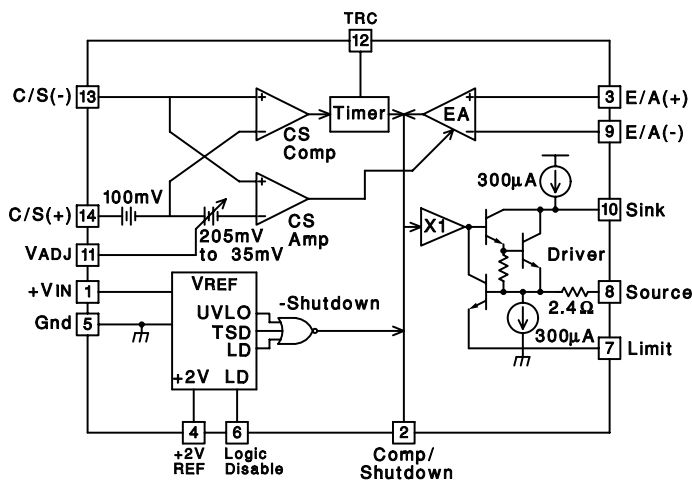
POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

Copyright © 2003, Texas Instruments Incorporated

UC2832-EP PRECISION LOW DROPOUT LINEAR CONTROLLER

SGLS147A – JANUARY 2003 – REVISED MARCH 2003

block diagram



Ordering Information

| | | | | | | |
|--------|---|---|----|---|----|----------------------------|
| UC 283 | 2 | T | DW | R | EP | |
| | | | | | | ENHANCED PLASTIC INDICATOR |
| | | | | | | TAPE and REEL INDICATOR |
| | | | | | | PACKAGE |
| | | | | | | DW = Plastic SOIC |
| | | | | | | TEMPERATURE INDICATOR |
| | | | | | | T = -40°C to 105°C |
| | | | | | | PRODUCT OPTION |



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

UC2832-EP PRECISION LOW DROPOUT LINEAR CONTROLLER

SGLS147A – JANUARY 2003 – REVISED MARCH 2003

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†‡

| | |
|--|--------------------------|
| V _{IN} supply voltage | 40 V |
| Driver output current (sink or source), I _O | 450 mA |
| Driver sink to source voltage | 40 V |
| TRC pin voltage | –0.3 V to 3.2 V |
| Other input voltages | –0.3 V to supply voltage |
| Operating junction temperature range, T _J | –55°C to 150°C |
| Storage temperature range, T _{stg} | –65°C to 150°C |
| Lead temperature soldering 1,6 mm (1/16 inch) from case for 10 seconds | 300°C |

† Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

‡ Unless otherwise indicated, voltages are reference to ground and currents are positive into and negative out of the specified terminals.

electrical characteristics, T_A = –40°C to 105°C for the UC2832T-EP, +V_{IN} = 15 V, Driver sink = +V_{IN}, C/S(+) voltage = +V_{IN}, and T_A = T_J (unless otherwise stated)

| PARAMETER | TEST CONDITIONS | MIN | TYP | MAX | UNITS | |
|---------------------------------|---|-----|-------|-----|-------|---|
| Input Supply | | | | | | |
| Supply current | +V _{IN} = 6 V | | 6.5 | 10 | mA | |
| | +V _{IN} = 36 V | | 9.5 | 15 | | |
| | Logic Disable = 2 V | | 3.3 | 10 | | |
| Reference Section | | | | | | |
| Output voltage | I _{DRIVER} = 10 mA | | 1.98 | 2 | 2.02 | V |
| | | | 1.96 | 2 | 2.04 | |
| Load regulation voltage | I _{OUT} = 0 to 10 mA | –10 | –5 | 10 | mV | |
| Line regulation | +V _{IN} = 4.5 V to 36 V, I _{DRIVER} = 10 mA | | 0.033 | 0.5 | mV/V | |
| Under-voltage lockout threshold | | | 3.6 | 4.5 | V | |
| Logic Disable Input | | | | | | |
| Threshold voltage | | 1.3 | 1.4 | 1.5 | V | |
| Input bias current | Logic Disable = 0 V | –5 | –1 | 0.1 | μA | |
| Current Sense Section | | | | | | |
| Comparator offset | T _J = 25°C | 95 | 100 | 105 | mV | |
| | T _J = Full range | 93 | 100 | 107 | | |
| Amplifier offset | V _{ADJ} = Open | 110 | 135 | 170 | mV | |
| | V _{ADJ} = 1 V | 180 | 235 | 290 | | |
| | V _{ADJ} = 0 V | 250 | 305 | 360 | | |
| Input bias current | V _{CM} = +V _{IN} | 65 | 100 | 135 | μA | |
| Input offset current | V _{CM} = +V _{IN} | –10 | | 10 | μA | |
| Amplifier CMRR | V _{CM} = 4.1 V to +V _{IN} + 0.3 V | | 80 | | dB | |
| Transconductance | I _{COMP} = ±100 μA | | 65 | | ms | |
| V _{ADJ} input current | V _{ADJ} = 0 V | –10 | –1 | | μA | |



UC2832-EP

PRECISION LOW DROPOUT LINEAR CONTROLLER

SGLS147A – JANUARY 2003 – REVISED MARCH 2003

electrical characteristics, $T_A = -40^\circ\text{C}$ to 105°C for the UC2832T-EP, $+V_{IN} = 15\text{ V}$, Driver sink = $+V_{IN}$, C/S(+) voltage = $+V_{IN}$, and $T_A = T_J$ (unless otherwise stated)

| PARAMETER | TEST CONDITIONS | MIN | TYP | MAX | UNITS | |
|--|--|-----------------------------------|------|------|---------------|---------------|
| Timer | | | | | | |
| Inactive leakage current | C/S(+) = C/S(-) = $+V_{IN}$, TRC pin = 2 V | | 0.25 | 1 | μA | |
| Active pull-up current | C/S(+) = $+V_{IN}$, TRC pin = 0 V, C/S(-) = $+V_{IN} - 0.4\text{ V}$ | -345 | -270 | -175 | μA | |
| Duty ratio (See Note 1) | ontime/period, $R_T = 200\text{ k}\Omega$, $C_T = 0.27\text{ }\mu\text{F}$ | | 4.8 | | % | |
| Period (See Notes 1 and 2) | ontime + offtime, $R_T = 200\text{ k}\Omega$, $C_T = 0.27\text{ }\mu\text{F}$ | | 36 | | ms | |
| Upper trip threshold (V_U) | | | 1.8 | | V | |
| Lower trip threshold (V_L) | | | 0.9 | | V | |
| Trip threshold ratio | V_U / V_L | | 2.0 | | V/V | |
| Error Amplifier Section | | | | | | |
| Input offset voltage | $V_{CM} = V_{COMP} = 2\text{ V}$ | -8 | | 8 | mV | |
| Input bias current | $V_{CM} = V_{COMP} = 2\text{ V}$ | -4.5 | -1.1 | | μA | |
| Input offset current | $V_{CM} = V_{COMP} = 2\text{ V}$ | -1.5 | | 1.5 | μA | |
| Open loop voltage gain (A_{VOL}) | $V_{COMP} = 1\text{ V}$ to 13 V | 50 | 70 | | dB | |
| Common mode rejection ratio (CMRR) | $V_{CM} = 0\text{ V}$ to $+V_{IN} - 3\text{ V}$ | 60 | 80 | | dB | |
| PSRR | $V_{CM} = 2\text{ V}$, $+V_{IN} = 4.5\text{ V}$ to 36 V | | 90 | | dB | |
| Transconductance | $I_{COMP} = \pm 10\text{ }\mu\text{A}$ | | 4.3 | | ms | |
| High-level output voltage (V_{OH}) | $I_{COMP} = 0$, Volts below $+V_{IN}$ | | 0.95 | 1.3 | V | |
| Low-level output voltage (V_{OL}) | $I_{COMP} = 0$ | | 4.5 | 0.7 | V | |
| Output high current (I_{OH}) | $V_{COMP} = 2\text{ V}$ | -700 | -500 | -100 | μA | |
| Output low current (I_{OL}) | $V_{COMP} = 2\text{ V}$ | C/S(-) = $+V_{IN}$ | 100 | 500 | 700 | μA |
| | | C/S(-) = $+V_{IN} - 0.4\text{ V}$ | 2 | 6 | | mA |

- NOTES: 1. These parameters are first-order supply-independent, however, both may vary with supply for $+V_{IN}$ less than about 4 V. This supply variation will cause a slight change in the timer period and duty cycle, although a high off-time/on-time ratio will be maintained.
 2. With recommended R_T value of 200 k Ω , $T_{OFF} \approx R_T C_T * \ln(V_U/V_L) \pm 10\%$.



UC2832-EP PRECISION LOW DROPOUT LINEAR CONTROLLER

SGLS147A – JANUARY 2003 – REVISED MARCH 2003

electrical characteristics, $T_A = -40^\circ\text{C}$ to 105°C for the UC2832T-EP, $+V_{IN} = 15\text{ V}$, Driver sink = $+V_{IN}$, C/S(+) voltage = $+V_{IN}$, and $T_A = T_J$ (unless otherwise stated)

| PARAMETER | TEST CONDITIONS | MIN | TYP | MAX | UNITS | |
|------------------------------------|---|---|-------|------|------------------|---------------|
| Driver Section | | | | | | |
| Maximum current | Driver limit and source pins common | $T_J = 25^\circ\text{C}$ | 200 | 300 | 400 | mA |
| | | $T_J = \text{Full range}$ | 100 | 300 | 450 | |
| Limiting voltage | Driver limit to source voltage at current limit, $I_{\text{SOURCE}} = -10\text{ mA}$, $T_J = 25^\circ\text{C}$, See Note 3 | | 0.72 | | V | |
| Internal current sense resistance | $T_J = 25^\circ\text{C}$, See Note 3 | | 2.4 | | Ω | |
| Pull-up current at driver sink | Compensation/Shutdown = 0.4 V | Driver sink = $+V_{IN} - 1\text{ V}$ | -800 | -300 | -100 | μA |
| | | $+V_{IN} = 36\text{ V}$, Driver sink = 35 V | -1000 | -300 | -75 | |
| Pull-down current at driver source | Compensation/Shutdown = 0.4 V, Driver source = 1 V | 150 | 300 | 700 | μA | |
| Saturation voltage sink to source | Driver source = 0 V, Driver current = 100 mA | | 1.5 | | V | |
| Maximum source voltage | Driver sink = $+V_{IN}$, Driver current = 100 mA, Volts below $+V_{IN}$ | | 3 | | V | |
| UVLO sink leakage | $+V_{IN} = \text{C/S}(+) = \text{C/S}(-) = 2.5\text{ V}$, Driver sink = 15 V, Driver source = 0 V, $T_A = 25^\circ\text{C}$ | | 25 | | μA | |
| Maximum reverse source voltage | Compensation/Shutdown = 0 V, $I_{\text{SOURCE}} = 100\ \mu\text{A}$, ($+V_{IN} = 3\text{ V}$) | | 1.6 | | V | |
| Thermal shutdown | | | 160 | | $^\circ\text{C}$ | |

NOTES: 3. The internal current limiting voltage has a temperature dependence of approximately $-2.0\text{ mV}/^\circ\text{C}$, or $-2800\text{ ppm}/^\circ\text{C}$. The internal $2.4\ \Omega$ sense resistor has a temperature dependence of approximately $+1500\text{ ppm}/^\circ\text{C}$.

APPLICATION AND OPERATION INFORMATION

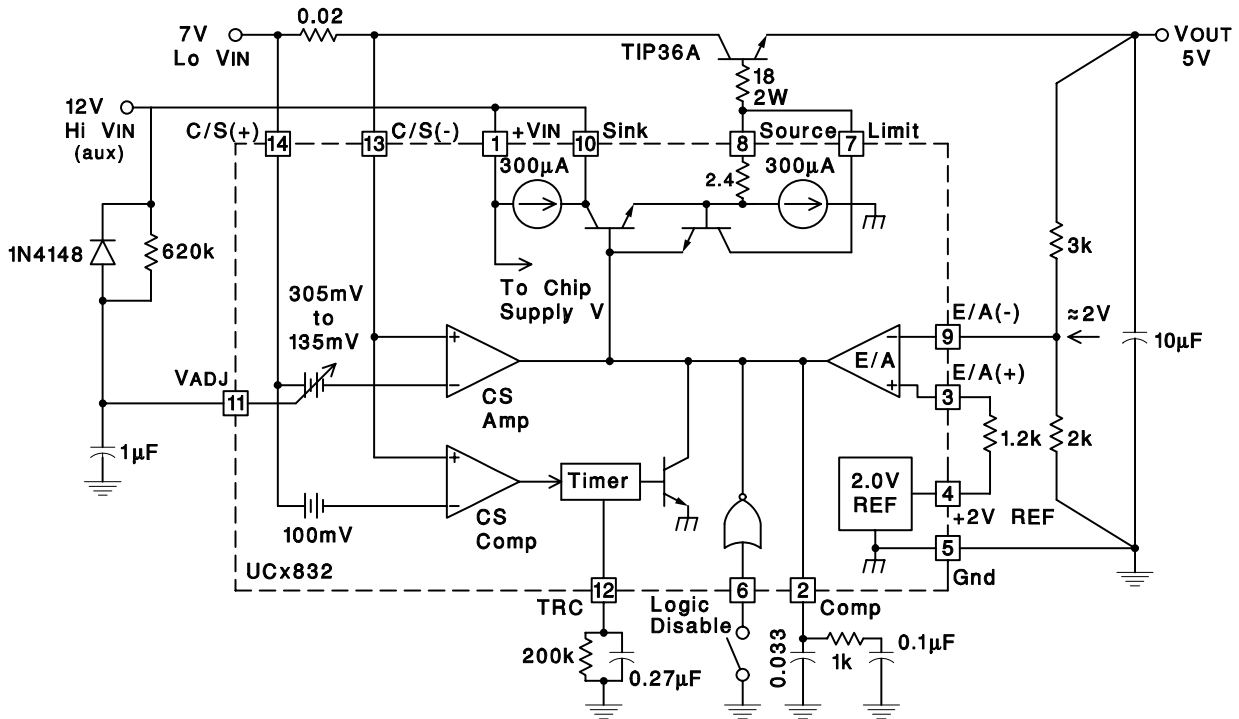


Figure 1. NPN Pass (Medium Power, Low Drop-Out Regulator)



UC2832-EP PRECISION LOW DROPOUT LINEAR CONTROLLER

SGLS147A – JANUARY 2003 – REVISED MARCH 2003

APPLICATION AND OPERATION INFORMATION

Estimating Maximum Load Capacitance

For any power supply, the rate at which the total output capacitance can be charged depends on the maximum output current available and on the nature of the load. For a constant-current current-limited power supply, the output will come up if the load asks for less than the maximum available short-circuit limit current.

To ensure recovery of a duty-ratio current-limited power supply from a short-circuited load condition, there is a maximum total output capacitance which can be charged for a given unit ON time. The design value of ON time can be adjusted by changing the timing capacitor. Nominally, $T_{ON} = 0.693 \times 10 \text{ k}\Omega \times C_T$.

Typically, the IC regulates output current to a maximum of $I_{MAX} = K \times I_{TH}$, where I_{TH} is the timer trip-point current, and

$$K = \frac{\text{Current Sense Amplifier Offset Voltage}}{100 \text{ mA}}$$

and is variable from 1.35 to 3.05 with V_{ADJ} .

For a worst-case constant-current load of value just less than I_{TH} , C_{MAX} can be estimated from:

$$C_{MAX} = \left(\frac{K - 1}{I_{TH}} \right) \times \left(\frac{T_{ON}}{V_{OUT}} \right)$$

where V_{OUT} is the nominal regulator output voltage.

For a resistive load of value R_L , the value of C_{MAX} can be estimated from:

$$C_{MAX} = \frac{T_{ON}}{R_L} \times \frac{1}{\ln \left[\left(1 - \frac{V_{OUT}}{K \times I_{TH} \times R_L} \right)^{-1} \right]}$$

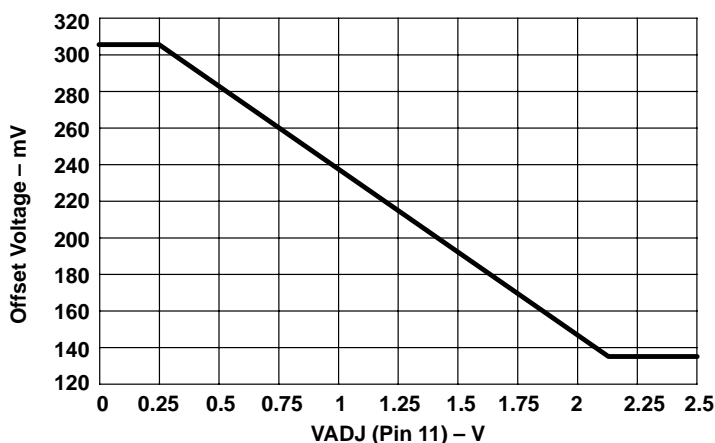


Figure 2. Current Sense Amplifier Offset Voltage vs V_{ADJ}

APPLICATION AND OPERATION INFORMATION

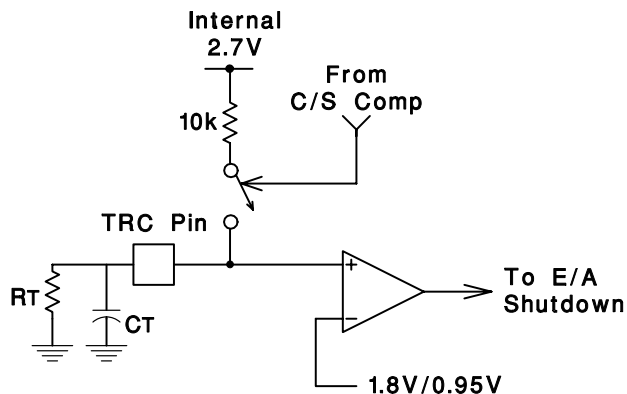
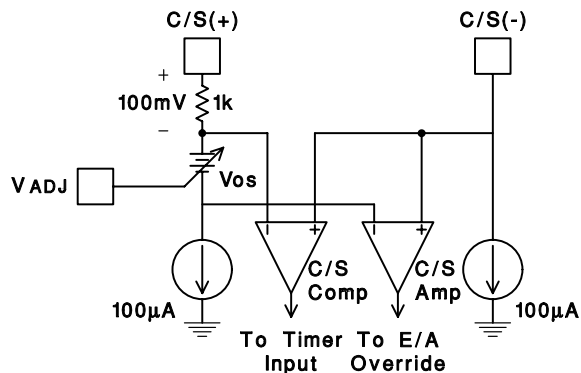


Figure 3. Timer Function



NOTE A: $V_{OS} = 205 \text{ mV to } 35 \text{ mV}$

Figure 4. Current Sense Input Configuration

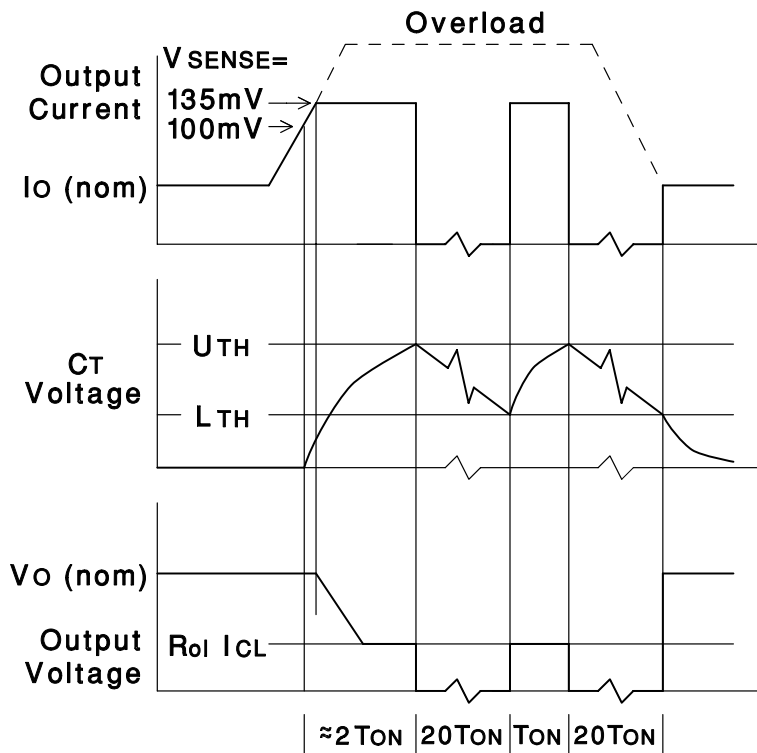


Figure 5. Load Current, Timing Capacitor Voltage, and Output Voltage of the Regulator Under Fault Conditions

UC2832-EP PRECISION LOW DROPOUT LINEAR CONTROLLER

SGLS147A – JANUARY 2003 – REVISED MARCH 2003

APPLICATION AND OPERATION INFORMATION

UCx832 Error Amplifier

AVOL vs Frequency and Cc

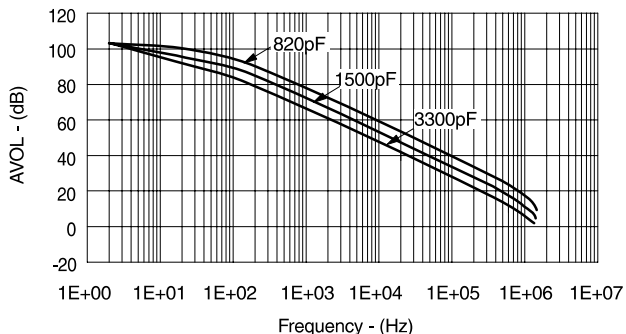


Figure 6. UCx832 Error Amplifier

UCx832 Current Sense Amplifier

AVOL vs Frequency and Cc

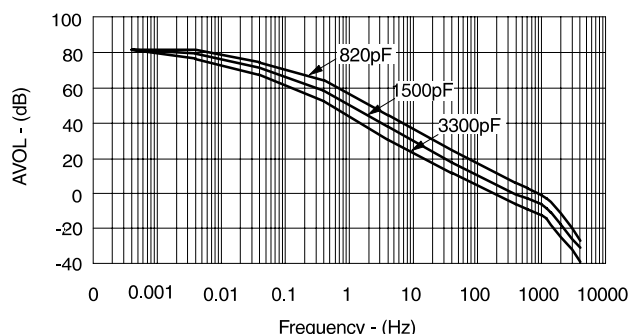


Figure 7. UCx832 Current Sense Amplifier

UCx832 Error Amplifier

Transconductance and Phase vs Frequency

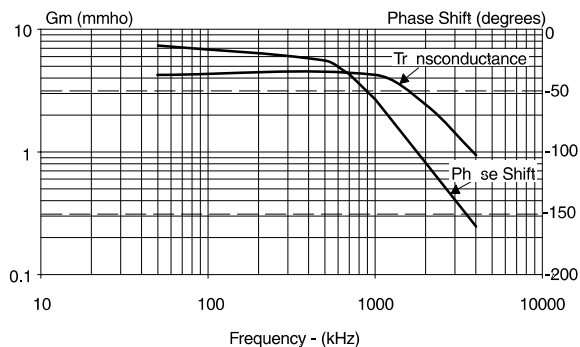


Figure 8. UCx832 Error Amplifier

UCx832 Current Sense Amplifier

Transconductance and Phase vs Frequency

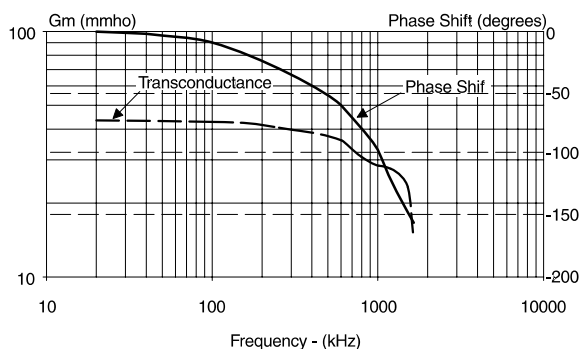


Figure 9. UCx832 Current Sense Amplifier

UC2832-EP PRECISION LOW DROPOUT LINEAR CONTROLLER

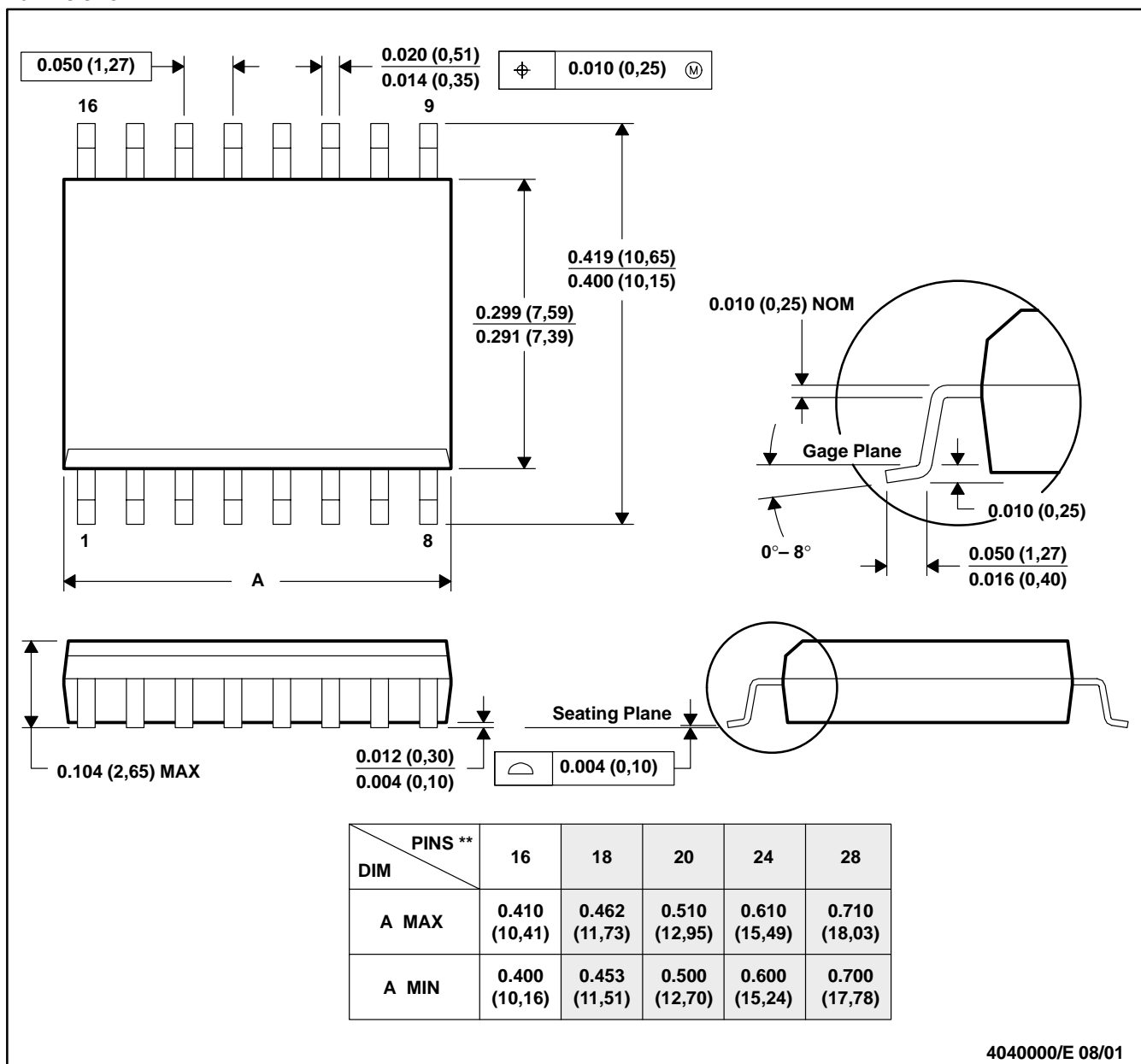
SGLS147A – JANUARY 2003 – REVISED MARCH 2003

MECHANICAL DATA

DW (R-PDSO-G)**

PLASTIC SMALL-OUTLINE PACKAGE

16 PINS SHOWN



- NOTES: B. All linear dimensions are in inches (millimeters).
 C. This drawing is subject to change without notice.
 D. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
 E. Falls within JEDEC MS-013

PACKAGING INFORMATION

| Orderable Device | Status ⁽¹⁾ | Package Type | Package Drawing | Pins | Package Qty | Eco Plan ⁽²⁾ | Lead/Ball Finish | MSL Peak Temp ⁽³⁾ |
|------------------|-----------------------|--------------|-----------------|------|-------------|-------------------------|------------------|------------------------------|
| UC2832TDWEP | ACTIVE | SOIC | DW | 16 | 40 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-2-260C-1 YEAR |
| UC2832TDWREP | ACTIVE | SOIC | DW | 16 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-2-260C-1 YEAR |
| V62/03633-01XE | ACTIVE | SOIC | DW | 16 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-2-260C-1 YEAR |

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSELETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

Important Information and Disclaimer:The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

OTHER QUALIFIED VERSIONS OF UC2832-EP :

- Catalog: [UC2832](#)

NOTE: Qualified Version Definitions:

- Catalog - TI's standard catalog product

TAPE AND REEL INFORMATION



QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

| Device | Package Type | Package Drawing | Pins | SPQ | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|--------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| UC2832TDWREP | SOIC | DW | 16 | 2000 | 330.0 | 16.4 | 10.85 | 10.8 | 2.7 | 12.0 | 16.0 | Q1 |

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

| Device | Package Type | Package Drawing | Pins | SPQ | Length (mm) | Width (mm) | Height (mm) |
|--------------|--------------|-----------------|------|------|-------------|------------|-------------|
| UC2832TDWREP | SOIC | DW | 16 | 2000 | 346.0 | 346.0 | 33.0 |

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Products

| | |
|-----------------------------|--|
| Amplifiers | amplifier.ti.com |
| Data Converters | dataconverter.ti.com |
| DSP | dsp.ti.com |
| Clocks and Timers | www.ti.com/clocks |
| Interface | interface.ti.com |
| Logic | logic.ti.com |
| Power Mgmt | power.ti.com |
| Microcontrollers | microcontroller.ti.com |
| RFID | www.ti-rfid.com |
| RF/IF and ZigBee® Solutions | www.ti.com/lprf |

Applications

| | |
|--------------------|--|
| Audio | www.ti.com/audio |
| Automotive | www.ti.com/automotive |
| Broadband | www.ti.com/broadband |
| Digital Control | www.ti.com/digitalcontrol |
| Medical | www.ti.com/medical |
| Military | www.ti.com/military |
| Optical Networking | www.ti.com/opticalnetwork |
| Security | www.ti.com/security |
| Telephony | www.ti.com/telephony |
| Video & Imaging | www.ti.com/video |
| Wireless | www.ti.com/wireless |

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2008, Texas Instruments Incorporated